Notice of References Cited					Application/Control No.  09/186,388  Examiner		Applicant(s)/Patent Under Reexamination LEE ET AL.		
							Art Unit		
					Ginette Peralta		Page 1 of 1 2814		of 1
U.S. PATENT DOCUMENTS									
*		DOCUMENT NO.	DATE		NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
		4.500.070	Dag 4005	Wu et al.		250	492.2	APS 🖾	OTHER
	A	4,560,879	Dec. 1985	vvu et al.		250	492.2		
	В								
	D								
	E								
	F								
	G								
	н								
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	J								
	К								
	L								
	М								
		FOREIGN PATENT DOCUMENTS						DOCUMENT	
*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS	SOURCE APS	OTHER
	N						-		
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	Q								
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	s								
	Т								
NON-PATENT DOCUMENTS DOCUMENT									
*	ŀ	DOCUMENT (Including Author, Title Date, Source, and Pertinent Pages)						SOURCE	**
								APS	OTHER
	U	S. Wolf, R. Tauber. Silicon Processing for the VLSI Era, Volume 1: Process Technology. Lattice Press, California, 1986. Pages 305-307							
	٧	S. Wolf. Silicon Processing for the VLSI Era, Volume 2: Process Integration. Lattice Press, California, 1990. Pages 332-334							⊠
	w								
	x								

<sup>\*</sup>A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a).)
\*\*APS encompasses any electronic search i.e. text, image, and Commercial Databases.
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PTO-892 (Rev. 03-98)

Notice of References Cited